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Greater Noida | Mathura

Monthly Updates

The Bulletin

A Newsletter from

Electronics & Communication Engineering Department.

RESEARCH

INNOVATION

NEW IDEAS

PLACEMENTS

GOALS

EVENTS

WORKSHOPS

GLAdiators
pushing the limits

Gems of
Agra (UP)

Congratulations

YASH AGARWAL
B.Tech. - EC (IV Year)
(Batch : 2025)

Package Offered

5.0

Lakh Per Annum

on being placed in



ZENUS GROUP



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GLAdiators
pushing the limits

Congratulations

HARSH MISRA

B.Tech. - EC (CS)
(IV Year)

on being placed in

KPIIT

Batch : 2025



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Office of Students' Welfare

Techनव्या
A vibrant blend of technology and vision
07-08 February, 2025

QUANTATHON



• Faculty Coordinators:
Dr. Manish Kumar: 9719232004
Dr. D.V. Prashant: 8109209873

• Student Coordinators:
Kartikayan Singh Tomar: 9758204599
Piyush Sharma: 9759949581

07th Feb, 2025

Venue : EC Conf. Hall
Time : 10:30 AM Onwards

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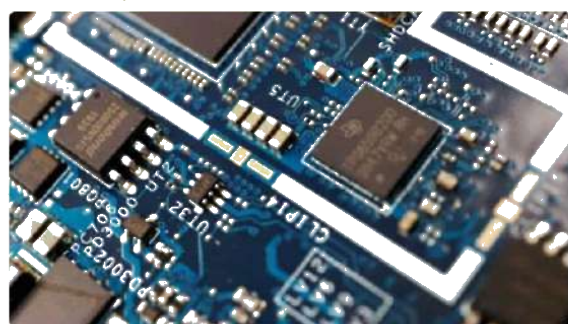
Admission Open
2025-26



B.Tech

Electronics and Communication Engineering

Source :
ZEEBUSINESS
हिंदी



अगले पांच साल में देश में दोगुनी होगी इलेक्ट्रॉनिक
मैन्युफैक्चरिंग, इस सेक्टर में आएंगी 50 लाख नौकरियां

8d 1.8k shares



Digital Data Protection Bill डिजिटल डाटा प्रोटेक्शन बिल का मसौदा तैयार
करने का काम एडवांस स्टेज में है. सरकार अब इस विधेयक के लिए इंडस्ट्री
कंसल्टेशन की ओर कदम बढ़ाने जा रही है



ADVANCING IN ELECTRONIC CIRCUIT DESIGN: OPPORTUNITIES AND FUTURE PROSPECTS

In today's rapidly evolving world of electronics, hands-on experience with tools like Cadence Virtuoso is essential for any aspiring engineer. During my learning process, I've worked on foundational projects such as Basic gates, CMOS inverters, current mirrors, ring oscillators, multiplexers and many more. These experiences have given me a solid grounding in both digital and analog circuit design, preparing me to meet real-world challenges in the semiconductor and electronics industries.

The field of VLSI design, which is the backbone of devices like smartphones, medical equipment, and automotive systems, plays a critical role in the modern tech landscape. As students, learning tools like Cadence Virtuoso can open doors to exciting career opportunities. From designing CMOS inverters to complex systems, we have the chance to contribute to innovations in IoT, wearable tech, and autonomous systems.

While mastering the basics of circuit design is crucial, one must go beyond theoretical concepts and engage in hand-on projects. By understanding layout design, performing Design Rule Checks (DRC), and using Layout Versus Schematic (LVS) techniques, students can ensure their designs are ready for real-world applications. This knowledge is essential for working in sectors like semiconductors, embedded systems, and RF design.

The industry demands constant innovation, and it's up to students to push the boundaries of technological advancement. Continuous learning, hands-on projects, and internships with companies in the electronics sector will help us shape the future. As the demand for more efficient, compact, and integrated systems rises, students have the opportunity to make an impact in areas like healthcare, communication, and sustainable technologies.








For those just beginning their journey in VLSI design, diving into projects and tools like Cadence Virtuoso can equip them with valuable skills. By building on these foundations, students can contribute to groundbreaking advancements in the electronics industry.



Aditya Kumar

B.Tech 2nd year in Electronics and Communication Engineering
GLA University

CONGRATULATIONS PLACED SIX STUDENTS

COURSE	STUDENTS NAME	COMPANY
B.Tech- EC (CS)	Ayush Srivastava	 HAVi DESIGN Your Technology Partner
B.Tech- EC (CS)	Prabal Agarwal	MIL POWER CONVERTER TECHNOLOGIES INDIA PVT. LTD. (ENERCON)
B.Tech- EC (CS)	Satviki Sahu	 interface
B.Tech- EC (CS)	Saurabh Sahariya	 interface
B.Tech- EC (CS)	Anshika	 interface
B.Tech- EC (CS)	Rudransh Pandey	 interface
B.Tech- EC	Sonu Gupta	 Rjal Don't Let Go The Right Way
B.Tech- EC (CS)	Vishakha Singhal	 Rjal Don't Let Go The Right Way

THINK BIG, BUILD SMALL: LET'S MINIATURIZE- THE OLED REVOLUTION



Dr. Jharna Agrawal
Assistant Professor,
ECE Department

OLED (Organic Light Emitting Diode) technology is reshaping the world of electronics, enabling thinner, lighter, and more energy-efficient devices. Unlike traditional LCDs, OLED panels emit their own light, eliminating the need for bulky backlighting. This has led to a surge in sleek smartphones, foldable displays, smartwatches, and high-end televisions, pushing the boundaries of innovation. The automotive, wearable tech, and AR/VR industries are also rapidly adopting OLED for its flexibility and superior visual performance.

Beyond displays, OLED is transforming the semiconductor industry. The demand for power-efficient OLED drivers and advanced chip architectures has fueled innovation in microelectronics and nanotechnology. Smaller, more efficient components are now being developed to support ultra-compact OLED micro displays used in AR, military optics, and medical devices. Manufacturing is also evolving. OLED's ability to be printed on flexible substrates has streamlined production, reduced material waste, and lowered energy consumption, making it a sustainable alternative to LCDs. As OLED expands into wearable healthcare, smart surfaces, and bio-integrated tech, it exemplifies miniaturization and efficiency, driving the future of electronics. With "Think Big, Build Small", OLED is paving the way for a smarter, greener, and more connected world.

Immersed in the rich tapestry of culture
at the **CCIS 2024** cultural night.

OUR TOP RECRUITERS



EDITORIAL TEAM

Chief Editor : Dr. Manish Kumar, Associate Professor

Editor : Mrs. Sweta, Assistant Professor

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