

Monthly Updates The Bulletin A Newsletter from

Electronics & Communication Engineering Department.

RESEARCHINNOVATIONNEW IDEASPLACEMENTSGOALSEVENTSWORKSHOPSTHE FUTURE IS ETCHED IN SILICON – AND WE ARE THE DESIGNERS

The Future is Etched in Silicon – And We Are the Designers

"चिप्स्यति भारतः, चिप्स्यामः वयम्"

("India will shine in semiconductors, and we will be the ones to make it happen.")

India's Semiconductor Revolution: From Consumers to Creators

India is on the brink of a semiconductor transformation. For years, we relied on imported chips, but with the India Semiconductor Mission (ISM), the rise of RISC-V, and domestic fab investments, we are building a self-reliant future.

The Shift is Happening

- ISM's \$10B push for domestic chip manufacturing.
- Homegrown RISC-V processors like Shakti and Vega.
- Investments by Tata, Micron, and ISRO in fabrication plants.
- Yet, technology needs skilled engineers—and that's where we step in.

My Journey: Learning by Building

- As a VLSI and Embedded Systems aspiring engineer, I have worked on:
- RISC-V processors for computing independence.
- FPGA-based AI accelerators for edge AI.
- Advanced VLSI projects bridging academia and industry.
- Analog IC Design + Embedded Engineering
- Hands-on experience, not just theory, drives innovation.

Vision: From Learning to Innovation

Semiconductor success depends on turning knowledge into real-world impact. My vision is to:



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- Promote hands-on learning—Design, test, and build, not just study.
- Bridge academia and industry—Align research with real-world chip design.
- Empower India's workforce—Contribute to open-source RISC-V, AI accelerators, and domestic chip projects.
- Explore next-gen technology—Al-driven chips, quantum computing, and post-Moore's Law architectures.

Our Role: Leading India's Semiconductor Future

- To take charge, we must:
- Master practical skills in VLSI, RISC-V, AI accelerators.
- Join open-source projects like Shakti and Vega.
- Bridge academia & industry by turning ideas into working silicon.
- Explore next-gen tech beyond Moore's Law.

The Time is Now

The semiconductor future is being built today. Who will lead it? We will.

"युवा शक्ति राष्ट्रस्य शक्तिः"

("The strength of the youth is the strength of the nation.")This is not just a vision—it is our responsibility. Let's build the future.

WISHING CIRCUIT CRAFTERS ALL THE BEST!

Innovation and competition go hand in hand, and every challenge presents an opportunity to learn, grow, and push boundaries. As the Circuit Crafters team—Priya, Trisha Gola, Astitva, Vishal Sharma—represented our college at the Analog Hackathon Finale, we extend our best wishes to them for their participation and efforts.

Hackathons are not just about winning but about the experience, problem-solving, and the teamwork that goes into tackling real-world challenges. Competing at such a level requires dedication, and making it to the finale is an achievement in itself. Regardless of the results, every competition is a stepping stone toward greater success.

To Circuit Crafters—keep innovating, keep building, and may this experience fuel your future endeavors in the world of electronics and VLSI. The journey matters just as much as the destination!



BOARDBUILDER BOOTCAMP: HANDS-ON PCB DESIGNING WORKSHOP

The Department of Electronics and Communication Engineering at GLA University is set to host the BoardBuilder Bootcamp, an exciting hands-on workshop on PCB Designing exclusively for 1st and 2nd-year ECE students. This session aims to introduce students to the fundamentals of PCB design, providing them with practical insights into the process of creating efficient circuit boards.

Led by Bhanu Pratap Singh Chauhan and Abhishek Sharma, students of ECE, this workshop will bridge the gap between theoretical learning and real-world applications. Participants will get hands-on experience with PCB design tools, layout techniques, and essential design principles that are crucial for electronics hardware development.

This initiative, organized under the Techtronica Club, is designed to empower students with practical knowledge, helping them build a strong foundation in electronics design. Whether you're passionate about embedded systems, hardware development, or VLSI, this workshop will provide valuable skills for your future projects.

Techtronica Department of **Electronics and Communication Engineering** BoardBuilder BOOTCAMP **MAR'25** Time: 10:00 AM Venue: 2002 (TI LAB) A Hands-on PCB Designing (For 1st & 2nd Year EC Students) Brijesh Maurya (7688827367) | Dheeraj Rajput (8273620558) GR. NOIDA CAMPUS GLAU ONLINE MATHURA CAMPUS +91-7617595602

Join us in BoardBuilder Bootcamp and take your first step into the world of professional PCB design!



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UDAAN 2025: IGNITING INNOVATION AND AMBITION

The Techtronic Society successfully organized Udaan 2025, an event designed to inspire creativity, technical excellence, and innovation among students. This initiative aimed to provide a platform where aspiring engineers could showcase their skills, collaborate on groundbreaking ideas, and engage in thought-provoking discussions.

Udaan 2025 featured a range of activities, including technical challenges, project demonstrations, and hands-on workshops. The event witnessed enthusiastic participation from students across various domains, fostering an environment of learning and exploration beyond conventional academics.

The success of Udaan was made possible by the relentless efforts of the organizing team and the active involvement of participants and mentors. Their dedication ensured that the event was not just a competition but an experience that encouraged students to dream big, innovate fearlessly, and take their ideas to new heights. As we reflect on this achievement, we look forward to continuing our journey of pushing boundaries and creating impactful opportunities for students. Udaan is not just an event; it is a movement toward a future shaped by innovation and excellence.



